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Transcript

Ref: MNE/Cohort-1/2238384 March 05, 2024

Name: Ritheshwar Manjunath
Programme: PG Level Advanced Certification Programme in Micro and Nanoelectronics
Period: March 2023 – February 2024
Final Grade: 'A'

Overall Performance (Based on the Grading Scheme given on the next page)

Assessments	Scores Obtained	Maximum Score	Percentage
Final Quizzes	277	350	79%
Assignments/ Project	178	300	59%
Total	**455**	**650**	**70%**

You have been graded as **'A'** with 70%.

Module-Wise Scores of All Attempts

Module Name	Final Quizzes	Assignments/ Project
Basics of Semiconductor Devices and Technology	80/100	-
Physics and Design of Transistors	82/100	-
Semiconductor Technology	82/100	-
Microelectronics Lab	-	133/200
VLSI Design Overview for Fab/Semiconductor Engineers	33/50	-
Reliability of Nanoscale circuits and systems	-	45/50
Design of Power Semiconductor Devices	-	Ab/50
Total	**277/350**	**178/300**

Prof. Mayank Shrivastava
Programme Co-ordinator, IISc

Grading Scheme - Part 1

Assessment Type	Total Count	Total Score
Final Quizzes	4	350
Assignments/ Project	3	300
		650

Grading Scheme - Part 2

Letter Grade	Percentage Range
A+	90% - 100%
A	70% - 89%
B+	50% - 69%
B	40% - 49%
C	0% - 39%

- Ab: Absent